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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/739,084	12/19/2003	Shunpei Yamazaki	0756-7232	9928	
31780 ERIC ROBINS	7590 01/23/200°	7	EXAMINER		
PMB 955	.011	DUDEK, JAMES A			
21010 SOUTHBANK ST. POTOMAC FALLS, VA 20165			ART UNIT	PAPER NUMBER	
TOTOMACTA	1000, 171 20103		. 2871		
SHORTENED STATUTOR	RY PERIOD OF RESPONSE MAIL DATE DELIVERY		Y MODE		
3 MONTHS		01/23/2007	PAPER		

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

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		Application No.	Applicant(s)					
Office Action Summan.		10/739,084	YAMAZAKI ET AL.					
	Office Action Summary	Examiner	Art Unit					
		James A. Dudek	2871					
Pe	 The MAILING DATE of this communication apperiod for Reply 	ears on the cover sheet with the c	orrespondence addr	ess				
	A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be tin will apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	N. hely filed the mailing date of this comi D (35 U.S.C. § 133).					
St	atus							
	1) Responsive to communication(s) filed on 11/2/	706 ·		·				
		action is non-final.						
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	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.							
		n parto Quayro, 1000 O.D. 11, 40	00 0.0. 210.	•				
Di	sposition of Claims							
	4) Claim(s) 1-5,9,11,13,15 and 17 is/are pending in the application.							
	4a) Of the above claim(s) is/are withdrawn from consideration.							
	5) Claim(s) is/are allowed.							
	6) Claim(s) <u>1-5,9,11,13,15 and 17</u> is/are rejected.							
	7) Claim(s) is/are objected to.		•					
	8) Claim(s) are subject to restriction and/or	r election requirement.						
Αŗ	oplication Papers							
	9) The specification is objected to by the Examine	r.		<i>;</i>				
	10)⊠ The drawing(s) filed on <u>12/19/03</u> is/are: a)⊠ a	ccepted or b) objected to by th	e Examiner.					
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).								
	Replacement drawing sheet(s) including the correct	ion is required if the drawing(s) is ob	jected to. See 37 CFR	1.121(d).				
	11) The oath or declaration is objected to by the Ex	aminer. Note the attached Office	Action or form PTO	-152.				
Pr	iority under 35 U.S.C. § 119	•						
	12)⊠ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a)⊠ All b)□ Some * c)□ None of:							
	1. Certified copies of the priority documents	s have been received.						
	2. Certified copies of the priority documents	s have been received in Applicati	on No					
	3. Copies of the certified copies of the prior	•	ed in this National St	tage				
	application from the International Bureau			-				
	* See the attached detailed Office action for a list	of the certified copies not receive	ed.					
Atı	achment(s)							
	Notice of References Cited (PTO-892)	4) Interview Summary						
2) 3)	Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)	Paper No(s)/Mail Da	ate Patent Application (PTO-1	52)				
J)	Paper No(s)/Mail Date illoz/06	6) Other:	atoni rippiioation (i 10°1	,				
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DETAILED ACTION

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1, 4-5, 9, 13, 15, 17 and 20 are rejected under 35 U.S.C. 103(a) as being unpatentable over US 20010022644 (644) in view of US 6300152 B1 (152).

Per claims 1, 4, 9 and 20, 644 teaches a card comprising: a display device comprising a first semiconductor element comprising a first polycrystalline semiconductor film [TFT in pixel section and see paragraph 162]; a first thin film integrated circuit comprising a second semiconductor element comprising a second polycrystalline semiconductor film [the analog switch TFT]; a first plastic substrate [130 and paragraph 125]; and a second plastic substrate [135 and paragraph 126], wherein the display device is electrically connected to the first thin film integrated circuit [pixel 144 is connected to pixel TFT], wherein the thin film integrated circuit and the display device are sealed [with a passivation layer 146] between the first substrate and the second substrate of the card [see figure 18, passivation layer 146], and wherein the first substrate and the second substrate comprise a plastic material [see paragraph 125]. 644 lacks the thin film integrated circuit and the display device sealed with a resin. However, 152 teaches, at column 1, lines 23-25, acrylic resin used as a passivation layer to increase the aperture ratio of the LCD and flatness of the layer.

Per claim 5, 644 teaches a card of claim 1 further comprising a second integrated circuit wherein the first and second thin film integrated circuits are laminated [each TFT is by the passivation laminate 146].

Per claims 13, 15 and 17, 644 teaches a card according to claims 1, wherein the card is an IC card [see paragraph 179].

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Claim 2 is rejected under 35 U.S.C. 103(a) as being unpatentable over US 20010022644 (644) in view of US 6300152 B1 (152) as applied to claims 1, 4-5, 9, 13, 15, 17 and 20 and further in view of US 4709991 A (991).

Per claims 2, 644 teaches a card according to claim 1, but lacks the card having a thickness from 0.05 mm through 1.5 mm. Since 644 uses the display in an IC card it would have to be relatively thin and thus its was desirable to make the display as thin as possible. US 4709991 A teaches techniques to make such thin displays. Accordingly, it would have been obvious to one of ordinary skill at the time of invention to combine the techniques of 991 in order to create a cell between 0.005 mm and 1.5 mm in order to place the display in an IC card.

Claim 3 is rejected under 35 U.S.C. 103(a) as being unpatentable over US 20010022644 (644) in view of US 6300152 B1 (152) as applied to claims 1, 4-5, 9, 13, 15, 17 and 20 and further in view of US 4709995 A (995).

644 lack a passive matrix electrode structure. However, 995 teaches, at column 1, that a passive matrix electrode structure compared to active matrix has a lower production cost. Accordingly, it would have been obvious to one of ordinary skill in the art at the time of invention to combine the passive matrix driving with 644.

of these limitations were well known because of the desire to create lighter cards, decrease manufacturing costs and reduce thickness by removing the light source, respectively.

Claim 11 is rejected under 35 U.S.C. 103(a) as being unpatentable over US 20010022644 (644) in view of US 6300152 B1 (152) as applied to claims 1, 4-5, 9, 13, 15, 17 and 20 and further in view of US 6859195 B2 (195)

644 lack the device being light emitting. However, 195 teaches at column 9, third paragraph that LCD and oled are interchangeable and that oled is self-emissive. Accordingly, it would have been obvious to one of ordinary skill at the time of invention to combine use a light emissive oled instead of an LCD.

Response to Arguments

Applicant's arguments with respect to the pending claims have been considered but are moot in view of the new ground(s) of rejection.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to James A. Dudek whose telephone number is 571-272-2290. The examiner can normally be reached on 9:00-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached on 571-272-1787. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

James A. Dudek Primary Examiner Art Unit 2871 Page 4